



STIC EIC 2100 Search Request Form

96886

Today's Date: 6.18.03

What date would you like to us to limit the search?

Priority Date: 6.12.98

Other:

Name Justin King
AU 2181 Examiner # 79007
Room # 2A08 CPKII Phone 305 457
Serial # 09/485,443

Format for Search Results (Circle One):

PAPER DISK EMAIL

Where have you searched so far?

USP DWPI EPO JPO ACM IBM TDB
IEEE INSPEC SPI Other EAST

Is this a "Fast & Focused" Search Request? (Circle One) YES NO

A "Fast & Focused" Search is completed in 2-3 hours (maximum). The search must be on a very specific topic and meet certain criteria. The criteria are posted in EIC2100 and on the EIC2100 NPL Web Page at <http://ptoweb/patents/stic/stic-tc2100.htm>.

What is the topic, novelty, motivation, utility, or other specific details defining the desired focus of this search? Please include the concepts, synonyms, keywords, acronyms, definitions, strategies, and anything else that helps to describe the topic. Please attach a copy of the abstract, background, brief summary, pertinent claims and any citations of relevant art you have found.

serial bus connection topology (1394 or USB)

- serial bus peripheral devices are connected in a "tree" topology
- each device has different speed and different number of ports
- the device with more port number is placed on the higher level in tree, so this helps to cut down the maximum tree level
- the device with higher speed is placed on the higher level in the tree, so the will support higher bus speed for the device at the lower level.

STIC Searcher Carol Wong Phone 305-9129
Date picked up 6-18-03 Date Completed 6-18-03





STIC Search Report

EIC 2100

STIC Database Tracking Number: 96886

TO: Justin King
Location: CPK2, 2A08
Art Unit : 2181
Wednesday, June 18, 2003

Cas Serial Number: 09/485443

From: Carol Wong
Location: EIC 2100
PK2-4B33
Phone: 305-9729

carol.wong@uspto.gov

Search Notes

Dear Examiner King,

Attached are the search results (from commercial databases) for your case.

Color tags mark the patents/articles which appear to be most relevant to the case. Pls resubmit the search tomorrow as a 'rush' request, if you have suggestions for additional terminology, or a different approach to searching the case.

Thanks,
Carol

File 347:JAPIO Oct 1976-2003/Feb(Updated 030603)
 (c) 2003 JPO & JAPIO
 File 350:Derwent WPIX 1963-2003/UD,UM &UP=200338
 (c) 2003 Thomson Derwent
 File 348:EUROPEAN PATENTS 1978-2003/Jun W01
 (c) 2003 European Patent Office
 File 349:PCT FULLTEXT 1979-2002/UB=20030612,UT=20030605
 (c) 2003 WIPO/Univentio

? ds

Set	Items	Description
S1	1705	AU='CHEN W':AU='CHEN W Z'
S2	1	AU='CHEN WEL'
S3	95	AU='CHEN WEI':AU='CHEN WEI-NING'
S4	3126	AU='LEE Y'
S5	1128	AU='LEE Y G':AU='LEE Y G L'
S6	1222	AU='LEE Y J'
S7	1	AU='LEE YOON'
S8	4	AU='LEE YOON JICK':AU='LEE YOON JIK'
S9	2	AU='LEE YUN GIK'
S10	5	S1:S3 AND S4:S9

? t10/ti/1-2

10/TI/1 (Item 1 from file: 350)

DIALOG(R)File 350:(c) 2003 Thomson Derwent. All rts. reserv.

Peak and hold calibration circuit for digital multimeter, has switching circuit connected to output of operational amplifier which switches capacitor connected between resistor and ground

10/TI/2 (Item 2 from file: 350)

DIALOG(R)File 350:(c) 2003 Thomson Derwent. All rts. reserv.

Electrical system for electronic fuel-injection motorcycle, has power management module, which divides electrical circuits into at least two power supply groups of different power supply priorities

? t10/9/3

10/9/3 (Item 3 from file: 350)

DIALOG(R)File 350:Derwent WPIX
 (c) 2003 Thomson Derwent. All rts. reserv.

012934072 **Image available**
 WPI Acc No: 2000-105919/200009
 XRPX Acc No: N00-081331

Topology optimization method for IEEE 1394 serial bus of multimedia instruments e.g. HDTV, DVD, DVC

Patent Assignee: SAMSUNG ELECTRONICS CO LTD (SMSU)

Inventor: CHEN W ; LEE Y G ; JIN W; LEE Y J

Number of Countries: 022 Number of Patents: 007

Patent Family:

Patent No	Kind	Date	Applicat No	Kind	Date	Week
WO 9964943	A2	19991216	WO 99KR291	A	19990612	200009 B
EP 1027640	A2	20000816	EP 99925442	A	19990612	200040
			WO 99KR291	A	19990612	
KR 2000001563	A	20000115	KR 9821903	A	19980612	200059
CN 1273652	A	20001115	CN 99800897	A	19990612	200115
KR 298979	B	20010906	KR 9821903	A	19980612	200227

JP 2002517967 W 20020618 WO 99KR291 A 19990612 200242
 JP 2000553880 A 19990612
 JP 3295074 B2 20020624 WO 99KR291 A 19990612 200243
 JP 2000553880 A 19990612

Priority Applications (No Type Date): KR 9821903 A 19980612

Patent Details:

Patent No	Kind	Lan	Pg	Main IPC	Filing Notes
WO 9964943	A2	E	23	G06F-000/00	
Designated States (National): CN JP US					
Designated States (Regional): AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE					
EP 1027640	A2	E		G06F-001/00	Based on patent WO 9964943
Designated States (Regional): DE GB					
KR 2000001563	A			H04L-012/28	
CN 1273652	A			G06F-013/14	
KR 298979	B			G11B-020/10	Previous Publ. patent KR 2000001563
JP 2002517967	W		25	H04L-012/28	Based on patent WO 9964943
JP 3295074	B2		7	H04L-012/28	Previous Publ. patent JP 200217967
Based on patent WO 9964943					

Abstract (Basic): WO 9964943 A2

NOVELTY - The serial bus comprises multiple nodes, each with communication ports and priority is assigned to nodes according to their count and transmission speed. Then unused port in node of first priority is connected to port in node of second priority and this process is continued until all nodes are connected.

DETAILED DESCRIPTION - The total port number of nodes is compared with reference value which varies with number of nodes, to determine whether or condition for topology optimization is satisfied. Priority is assigned to nodes only if the condition is satisfied.

USE - For IEEE 1394 serial bus used in multimedia instruments such as HDTV, DVD, DVC.

ADVANTAGE - Enables construction of topology which increases speed capacity of each node in bus.

DESCRIPTION OF DRAWING(S) - The figure shows the flow chart illustrating topology optimization method.

pp; 23 DwgNo 2/4

Title Terms: TOPOLOGICAL; OPTIMUM; METHOD; SERIAL; BUS; INSTRUMENT; HDTV
 Derwent Class: T01

International Patent Class (Main): G06F-000/00; G06F-001/00; G06F-013/14; G11B-020/10; H04L-012/28

International Patent Class (Additional): G06F-013/00; G06F-013/18; G06F-013/20; G06F-013/26; G06F-013/28; G06F-013/30; G06F-013/36; G06F-013/38; H04L-012/44

File Segment: EPI

Manual Codes (EPI/S-X): T01-C07C5; T01-C07D; T01-J30

? t10/5/4-5

10/5/4 (Item 1 from file: 348)

DIALOG(R)File 348:EUROPEAN PATENTS

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01120233

METHOD OF OPTIMIZING THE TOPOLOGY OF THE IEEE 1394 SERIAL BUS

VERFAHREN ZUM OPTIMIEREN DER TOPOLOGIE EINES IEEE 1394 SERIENBUSSES

PROCEDE D'OPTIMISATION DE LA TOPOLOGIE DU BUS SERIE IEEE 1394

PATENT ASSIGNEE:

Samsung Electronics Co., Ltd., (2419972), 416 Maetan-dong, Paldal-ku, Suwon-shi, Kyungki-do 442-370, (KR), (Applicant designated States: all)

INVENTOR:

CHEN, Wei , Chamshiljugong Apt. 270-402, Chamshil-dong, Songpa-gu,

Seoul 138-220, (KR)
LEE, Yun, Gik , 326-2, Tangsan-dong 6-Ga, Yongdungpo-gu, Seoul 150-040,
(KR)
LEGAL REPRESENTATIVE:
Grunecker, Kinkeldey, Stockmair & Schwanhausser Anwaltssozietat (100721)
, Maximilianstrasse 58, 80538 Munchen, (DE)
PATENT (CC, No, Kind, Date): EP 1027640 A2 000816 (Basic)
WO 9964943 991216
APPLICATION (CC, No, Date): EP 99925442 990612; WO 99KR291 990612
PRIORITY (CC, No, Date): KR 9821903 980612
DESIGNATED STATES: DE; GB
INTERNATIONAL PATENT CLASS: G06F-001/00
NOTE:
No A-document published by EPO
LEGAL STATUS (Type, Pub Date, Kind, Text):
Application: 000816 A2 Published application without search report
Application: 20000216 A2 International application. (Art. 158(1))
Change: 030129 A2 Title of invention (German) changed: 20021206
Examination: 000816 A2 Date of request for examination: 20000211
Change: 030122 A2 Title of invention (German) changed: 20021202
Application: 20000216 A2 International application entering European
phase
LANGUAGE (Publication,Procedural,Application): English; English; English

10/5/5 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00533591 **Image available**
METHOD OF OPTIMIZING THE TOPOLOGY OF THE IEEE 1394 SERIAL BUS
PROCEDE D'OPTIMISATION DE LA TOPOLOGIE DU BUS SERIE IEEE 1394
Patent Applicant/Assignee:
SAMSUNG ELECTRONICS CO LTD,
CHEN Wei,
LEE Yun Gik,
Inventor(s):
CHEN Wei ,
LEE Yun Gik
Patent and Priority Information (Country, Number, Date):
Patent: WO 9964943 A2 19991216
Application: WO 99KR291 19990612 (PCT/WO KR9900291)
Priority Application: KR 9821903 19980612
Designated States: CN JP US AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL
PT SE
Main International Patent Class: G06F
Publication Language: English
Fulltext Availability:
Detailed Description
Claims
Fulltext Word Count: 1934

English Abstract

A method of optimizing the topology of the IEEE 1394 serial bus having a plurality of nodes each with communication ports, comprises the steps of prioritizing the nodes according to the number of the ports and the transmission speed, connecting a non-used port of the node of the first priority with a port of the node of the second priority, and repeating the previous step until all of the nodes are connected together, whereby the nodes are connected through the ports according to priority order.

French Abstract

Ce procede d'optimisation de la topologie du bus serie IEEE 1394 comprenant plusieurs noeuds, chacun dote de ports de communication, comprend les etapes consistant a classer par priorite les noeuds en fonction du nombre de ports et de la vitesse de transmission, a connecter le port non utilise du noeud possedant une premiere priorite avec un port du noeud possedant une seconde priorite, et a repeter cette etape jusqu'a ce que tous les noeuds soient connectes ensemble, les noeuds etant ainsi connectes a travers les ports, en fonction de leur ordre de priorite.

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